

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application:

T. WATANABE et al

Serial No. 09/198,658 Filed: November 24, 1998

Group Art Unit:

2783

Examiner:

E. Coleman

For:

NEURAL NETWORK PROCESSING SYSTEM

USING SEMICONDUCTOR MEMORIES

## PRELIMINARY AMENDMENT

Assistant Commissioner of Patents Washington, D.C. 20231

Sir:

Prior to examination, please amend the above application as follows.

## IN THE SPECIFICATION

Attached is a Substitute Specification, as filed in the original parent application, U.S. Serial No. 07/928,755, along with a "marked-up" copy of the original specification noting the changes made thereto. Applicants respectfully submit that no new matter has been added or presented in the Substitute Specification.

## IN THE CLAIMS

Please cancel claim 1 and add new claim 25 as set forth below.